

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	174	semiconductor and (high adj voltage) and (low adj voltage) and ((recess or groove or trench or LOCOS) same ((edge or bird\$2 or beak) with (exten\$4 or protru\$4 or project\$3))) and (isolation or insulat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/21 10:50
L5	586	semiconductor and (high adj voltage) and (low adj voltage) and ((recess or groove or trench or LOCOS) same (edge or bird or beak)) and (step) and @ad<"20030216"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 10:58
L1	306	257/500.ccls. and @ad<"20030416"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:04
L2	500	(257/395-397).ccls. and @ad<"20030416"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:23
L3	177	(257/395-397).ccls. and @ad<"20030416" and edge	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:28
L4	1033	257/350.ccls. and @ad<"20030416"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 11:31